

### **Amendments to the Claims:**

Please amend the claims as follows:

1. (Currently Amended) A receiver comprising a decoder for receiving encoded video signals ~~which include~~produced by an encoder in a transmitter, the encoder being selectively operative to transmit only differences between selected macroblocks in successive frames when a specific difference criteria between said successive blocks is not exceeded, together with a mode indicator signal indicative of whether or not the last macroblock was encoded in inter-frame prediction format or not; the receiver also comprising:

a video signal error detection system comprising a first comparator for comparing macroblocks in successive frames output from said decoder and applying said specific difference criteria to provide an indication of whether inter-frame prediction should apply or not, and a second comparator for comparing the indication from the first comparator with said mode indicator signal,~~an output direct from the decoder indicative of whether or not the last received macroblock was encoded in inter-frame prediction format or not,~~ the second comparator being operative to generate an error signal when a divergence is detected.

2. (Previously Amended) A system according to Claim 1, including an error concealment circuit responsive to the decoder and a previous frames buffer to recover corrupted data when the second comparator indicates an error.

3. (Previously Amended) A system according to Claim 1, wherein the first comparator acts as a mode decision circuit capable of generating one of three outcomes namely, inter-frame prediction, no inter-frame prediction and unknown.

4. (Previously Amended) A system according to Claim 3, wherein the mode decision circuit responds to each macroblock of each frame from the output from the decoder to determine a mean value for pixels and responds to a difference A between

reconstructed values of pixels in said macroblock and said mean value, a difference B between the reconstructed values of pixels of the present macroblock and reconstructed values of pixels of the corresponding macroblock of an immediately preceding frame and an error margin E to provide a first outcome if  $A < B - E$  a second outcome if  $B - E < A < B + E$  and a third outcome if  $B + E < A$ .

5. (Original) A system according to Claim 2, wherein the error concealment circuit acts to replace each corrupted macroblock with a corresponding macroblock in the immediately preceding frame.

6. (Previously Added) A video signal error detection system for use in a receiver having a decoder for receiving encoded video signals produced by an encoder in a transmitter, the encoder being selectively operative to transmit only differences between selected macroblocks in successive frames when a specific difference criteria between said successive blocks is not exceeded, the detection system comprising a first comparator for comparing macroblocks in successive frames in an output signal from said decoder, applying said specific difference criteria to provide an indication of whether inter-frame prediction should apply or not, and a second comparator for comparing the indication from the first comparator with an output from the decoder indicative of whether or not the last received macroblock was in inter-frame prediction format or not and operable to generate an error signal when a divergence is detected; wherein the first comparator acts as a mode decision circuit capable of generating one of three outcomes namely, inter-frame prediction, no inter-frame prediction and unknown; wherein the mode decision circuit responds to each macroblock of each frame from the output from the decoder to determine a mean value for pixels and responds to a difference A between reconstructed values of pixels in said macroblock and said mean value, a difference B between the reconstructed values of pixels of the present macroblock and reconstructed values of pixels of the corresponding macroblock of an immediately preceding frame and an error margin E to provide a first outcome if  $A < B - E$  a second outcome if  $B - E < A < B + E$  and a third outcome if  $B + E < A$ .

7. Cancelled.

8. Cancelled.

9. Cancelled.